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SECTION II—CLAIMS

1. (Currently Amended) A process comprising:

providing a wafer, the wafer comprising an inter-layer dielectric (1 D) having a feature therein, an under-layer deposited on the ILD, a barrier 1 yer deposited on the under-layer and a conductive layer deposited on the barrier lay r;

exposing the barrier layer;

placing the wafer in a holder that seals the edges thereof, such that, v hen the holder and the wafer are immersed in an electrolyte, the electrolyte will nly affect a surface of the wafer;

immersing the holder and the wafer in the electrolyte, such that at least the barrier layer is wholly immersed in the electrolyte; and

applying an electrical potential between the wafer and an elect ode immersed in the electrolyte until at least part of the barrier layer is removed.

- 2. (Original) The process of claim 1 wherein the conductive layer is copper.
- 3. (Original) The process of claim 1 wherein the barrier layer comprises ruther um (Ru), rhodium (Rh), tantalum (Ta), iridium (Ir), osmium (Os), or alloys the eof containing nitrogen (N), silicon (Si) or carbon (C).
- 4. (Original) The process of claim 1 wherein the under-layer is titanium (Ti), titar um nitride (TiN), tungsten (W), tungsten nitride (WN) or tantalum nitride (TaN).
- 5. (Original) The process of claim 1, further comprising removing at least a portic 1 of the under-layer using chemical mechanical polishing (CMP).

- 6. (Original) The process of claim 1 wherein the electrolyte has a pH equal t or greater than 10.
- (Original) The process of claim 6 wherein the electrolyte comprises a solutio t of
 potassium hydroxide (KOH), sodium hydroxide (NaOH), ammonium hydroxide
 (NH₄OH) or tetra-methyl ammonium hydroxide (TMAH).
- 8. (Original) The process of claim 1, further comprising adding an additive to the electrolyte.
- (Original) The process of claim 8 wherein the additive is an oxidizer, a corro ion inhibitor, a surfactant, a buffer, a complexor, or combinations thereof.
- 10. (Original) The process of claim 1 wherein the electrical potential has a value e ual to or greater than 0.5V with respect to the saturated calomel reference electrode
- 11. (Original) The process of claim 1, further comprising removing at least a portio 1 of the conductive layer using chemical mechanical polishing (CMP).
- 12. (Currently Amended) A process comprising:

providing a wafer, the wafer comprising an inter-layer dielectric (I.D) having a feature therein, an under-layer deposited on the ILD, and a barrier l yer deposited on the under-layer, and a conductive layer deposited in the feature;

placing the wafer in a holder that seals the edges thereof, such that, v ien the holder and the wafer are immersed in an electrolyte, the electrolyte will affect a surface of the wafer;

immersing the holder and the wafer in the electrolyte, such that at leas the barrier layer is wholly immersed in the electrolyte; and

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applying an electrical potential between the wafer and an elect ode immersed in the electrolyte until at least part of the barrier layer is removed.

- 13. (Original) The process of claim 12 wherein the conductive layer is copper.
- 14. (Original) The process of claim 12 wherein the barrier layer comprises ruther um (Ru), rhodium (Rh), tantalum (Ta), iridium (Ir), osmium (Os), or alloys the eof containing nitrogen (N), silicon (Si) or carbon (C).
- 15. (Original) The process of claim 12 wherein the under-layer is titanium Ii), titanium nitride (TiN), tungsten (W), tungsten nitride (WN) or tantalum ni ide (TaN).
- 16. (Original) The process of claim 12, further comprising removing at least a poi ion of the under-layer using chemical mechanical polishing (CMP).
- 17. (Original) The process of claim 12 wherein the electrolyte has a pH equal t or greater than 10.
- 18. (Original) The process of claim 17 wherein the electrolyte comprises a solutio 1 of potassium hydroxide (KOH), sodium hydroxide (NaOH), ammonium hydroxide (NH4OH) or tetra-methyl ammonium hydroxide (TMAH).
- (Original) The process of claim 12, further comprising adding an additive to the electrolyte.
- 20. (Original) The process of claim 19 wherein the additive is an oxidizer, a corro ion inhibitor, a surfactant, a buffer, a complexor, or combinations thereof.
- 21. (Original) The process of claim 12 wherein the electrical potential has a value e ual to or greater than 0.5V with respect to the saturated calomel reference electrode

- 22. (Original) The process of claim 12, further comprising removing at least a polition of the conductive layer using chemical mechanical polishing (CMP).
- 23.-33. (Canceled)
- 34. (Currently Amended) A process comprising:

providing a wafer, the wafer comprising an inter-layer dielectric (I D) having a feature therein, an under-layer deposited on the ILD, a barrier 1 yer deposited on the under-layer and a conductive layer deposited on the barrier lay r;

exposing the barrier layer; and

placing the wafer in a holder that seals the edges thereof, such that, v ien the holder and the wafer are immersed in an electrolyte, the electrolyte will inly affect a surface of the wafer;

immersing the holder and the wafer in the electrolyte, such that at least the barrier layer is wholly immersed in the electrolyte; and

electrolytically removing at least part of the barrier layer using [[a base] an electrolyte.

- 35. (Previously Presented) The process of claim 34 wherein the conductive layer is copper.
- 36. (Previously Presented) The process of claim 34 wherein the barrier layer composes ruthenium (Ru), rhodium (Rh), tantalum (Ta), iridium (Ir), osmium (Os), or al pys thereof containing nitrogen (N), silicon (Si) or carbon (C).

- 37. (Previously Presented) The process of claim 34 wherein the under-layer is tital ium (Ti), titanium nitride (TiN), tungsten (W), tungsten nitride (WN) or tantalum ni ride (TaN).
- 38. (Previously Presented) The process of claim 34, further comprising removir; at least a portion of the under-layer using chemical mechanical polishing (CMP).
- 39. (Previously Presented) The process of claim 34 wherein the electrolyte has a pH equal to or greater than 10.
- 40. (Previously Presented) The process of claim 39 wherein the electrolyte comprises a solution of potassium hydroxide (KOH), sodium hydroxide (NaOH), ammor um hydroxide (NH4OH) or tetra-methyl ammonium hydroxide (TMAH).
- 41. (Previously Presented) The process of claim 34, further comprising adding an additive to the electrolyte.
- 42. (Previously Presented) The process of claim 41 wherein the additive is an oxid zer, a corrosion inhibitor, a surfactant, a buffer, a complexor, or combinations theree?
- 43. (Previously Presented) The process of claim 34 wherein the electrical potential has a value equal to or greater than 0.5V with respect to the saturated calomel reference electrode.
- 44. (Previously Presented) The process of claim 34, further comprising removin; at least a portion of the conductive layer using chemical mechanical polishing (CN P).